pbs

DECLARATION (37 CFR 1.63) FOR UTILITY OR DESIGN PATENT APPLICATION USING AN APPLICATION DATA SHEET (37 CFR 1.76)

Title Of Invention:	Docket No.	X-1213 US

DELAY LOCK LOOP USING SHIFT REGISTER WITH TOKEN BIT TO SELECT ADJACENT CLOCK SIGNALS

As a below named inventor(s), I/We declare that:

This declaration is directed to:

[X] The attached application, or

[ ] Application No._____, filed on _____

  [ ] as amended (on) _____ (if applicable);

I/We believe I/We am/are the original and first inventor(s) of the subject matter which is claimed and for which a patent is sought;

I/We have reviewed and understand the contents of the above-identified application, including the claims, as amended by any amendment specifically referred to above;

I/We acknowledge the duty to disclose to the United States Patent and Trademark Office all information known to me/us to be material to patentability as defined in 37 CFR 1.56, including material information which became available while the application was pending in the United States Patent and Trademark Office; and

All statements made herein of my own knowledge are true, all statements made herein on information and belief are believed to be true, and further that these statements were made with the knowledge that willful false statements and the like are punishable by fine or imprisonment, or both, under 18 U.S.C. 1001, and may jeopardize the validity of the application or any patent issuing thereon.

FULL NAME OF INVENTOR(S)

Inventor One:	**Andy T. Nguyen**	Date	7/22/2003

Signature: _____ Citizen of: **United States**

Inventor Two:	**Shi-dong Zhou**	Date	7/22/2003

Signature: _____ Citizen of: **United States**

Inventor Three: _____ Date _____

Signature: _____ Citizen of: _____

Inventor Four: _____ Date _____

Signature: _____ Citizen of: _____

[ ] Additional inventors are being named on_____additional form(s) attached hereto.

LDC:pbs